	Application No.	Applicant(s)
Notice of Allowability		
	10/608,295 Examiner	ARAI ET AL.
	Examiner	Artonit
	Junghwa M. Im	2811
The MAILING DATE of this communication appeal All claims being allowable, PROSECUTION ON THE MERITS IS herewith (or previously mailed), a Notice of Allowance (PTOL-85) NOTICE OF ALLOWABILITY IS NOT A GRANT OF PATENT R of the Office or upon petition by the applicant. See 37 CFR 1.313	(OR REMAINS) CLOSED in this or other appropriate communical IGHTS. This application is subjection.	application. If not included ation will be mailed in due course. THIS
1. This communication is responsive to <u>papers filed 20 June</u> .	<u>2006</u> .	
2. The allowed claim(s) is/are <u>1-20</u> .		•
 3. Acknowledgment is made of a claim for foreign priority una) All b) Some* c) None of the: 1. Certified copies of the priority documents have 2. Certified copies of the priority documents have 	e been received.	
3. Copies of the certified copies of the priority documents have been received in this national stage application from the		
International Bureau (PCT Rule 17.2(a)).		
* Certified copies not received:		
Applicant has THREE MONTHS FROM THE "MAILING DATE" noted below. Failure to timely comply will result in ABANDONN THIS THREE-MONTH PERIOD IS NOT EXTENDABLE.		eply complying with the requirements
4. A SUBSTITUTE OATH OR DECLARATION must be subm INFORMAL PATENT APPLICATION (PTO-152) which give		
5. CORRECTED DRAWINGS (as "replacement sheets") must	st be submitted.	
(a) including changes required by the Notice of Draftspers		TO-948) attached
1) 🗌 hereto or 2) 🔲 to Paper No./Mail Date		
(b) ☐ including changes required by the attached Examiner' Paper No./Mail Date	s Amendment / Comment or in the	ne Office action of
Identifying indicia such as the application number (see 37 CFR 1 each sheet. Replacement sheet(s) should be labeled as such in t	.84(c)) should be written on the dr he header according to 37 CFR 1.	awings in the front (not the back) of I21(d).
6. DEPOSIT OF and/or INFORMATION about the depo attached Examiner's comment regarding REQUIREMENT		
Attachment(s)	5 D Natice of Inform	al Datant Application
 Notice of References Cited (PTO-892) Notice of Draftperson's Patent Drawing Review (PTO-948) 	 5. ☐ Notice of Inform 6. ☐ Interview Summ 	
3. ☐ Information Disclosure Statements (PTO/SB/08),	Paper No./Mail 7. ⊠ Examiner's Ame	Date
Paper No./Mail Date		ement of Reasons for Allowance
of Biological Material	9. Other	EDDIE LEE BY PATENT EXAMINER
	TECHNOI	OGY CENTER 2800

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DETAILED ACTION

Election/Restrictions

Claims 1-12 and 20 are directed to an allowable product. Pursuant to the procedures set forth in MPEP § 821.04(B), claims 13-19, directed to the process of making or using an allowable product, previously withdrawn from consideration as a result of a restriction requirement, are hereby rejoined and fully examined for patentability under 37 CFR 1.104.

Because all claims previously withdrawn from consideration under 37 CFR 1.142 have been rejoined, the restriction requirement as set forth in the Office action mailed on August 20, 2004 is hereby withdrawn. In view of the withdrawal of the restriction requirement as to the rejoined inventions, applicant(s) are advised that if any claim presented in a continuation or divisional application is anticipated by, or includes all the limitations of, a claim that is allowable in the present application, such claim may be subject to provisional statutory and/or nonstatutory double patenting rejections over the claims of the instant application. Once the restriction requirement is withdrawn, the provisions of 35 U.S.C. 121 are no longer applicable. See *In re Ziegler*, 443 F.2d 1211, 1215, 170 USPQ 129, 131-32 (CCPA 1971). See also MPEP § 804.01.

Allowable Subject Matter

Claims 1-20 are allowed.

The following is an examiner's statement of reasons for allowance.

The reference of record fails to teach or suggest, either singularly or in combination at least the limitation of a stacked semiconductor configuration wherein the uppermost semiconductor chip is mounted with a circuit-formation surface facing the substrate and

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electrically connected to the lower semiconductor chip through the plurality of the bumps, and the encapsulation resin layer is formed so that the top surface and the portions of the side surfaces adjacent to the top surface of the uppermost semiconductor chip is exposed to the outside of the encapsulation resin layer and the circuit-formation surface and the portions of the side surfaces adjacent to the circuit-formation surface of the uppermost semiconductor chip are covered with the encapsulation resin.

Additionally, the reference of record fails to teach or suggest, either singularly or in combination at least the limitation of the method claim for mounting semiconductors in a stacked configuration wherein the uppermost semiconductor chip is mounted with a circuit-formation surface facing the substrate and the encapsulation resin layer is formed so that the top surface and the portions of the side surfaces adjacent to the top surface of the uppermost semiconductor chip is exposed to the outside of the encapsulation resin layer and the circuit-formation surface and the portions of the side surfaces adjacent to the circuit-formation surface of the uppermost semiconductor chip are covered with the encapsulation resin.

Any comments considered necessary by applicant must be submitted no later than the payment of the issue fee and, to avoid processing delays, should preferably accompany the issue fee. Such submissions should be clearly labeled "Comments on Statement of Reasons for Allowance."

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Conclusion

Any inquiry concerning this communication or earlier communications from the examiner should be directed to Junghwa M. Im whose telephone number is (571) 272-1655. The examiner can normally be reached on MON.-FRI. 8:30AM-5:00PM.

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Eddie C. Lee can be reached on (571) 272-1732. The fax phone number for the organization where this application or proceeding is assigned is 571-273-8300.

Information regarding the status of an application may be obtained from the Patent Application Information Retrieval (PAIR) system. Status information for published applications may be obtained from either Private PAIR or Public PAIR. Status information for unpublished applications is available through Private PAIR only. For more information about the PAIR system, see http://pair-direct.uspto.gov. Should you have questions on access to the Private PAIR system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll-free). If you would like assistance from a USPTO Customer Service Representative or access to the automated information system, call 800-786-9199 (IN USA OR CANADA) or 571-272-1000.

jmi

8/31/2006

SUPERVISORY PATENT EXAMINER TECHNOLOGY CENTER 2800